

DESIGNING A FOWARD AND REVERSE BIASED DIODES

BY

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**A PROJECT REPORT SUBMITTED TO THE DEPARTMENT OF PHYSICS IN
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ABSTRACT

This research aimed at Designing a forward and reverse bias diode that is applied in many areas like rectification, radios, logic gates (**ON** and **OFF**)

The research was guided by the following objectives, construction of a pn junction diode, working principle of the pn junction and biasing of a diode.

The main focus of the project is on designing a forward and reverse biased diode using the semiconductor.

DECLARATION

I **YEKO ERIC** hereby declare that this project is my own original work with exceptions from literature review and quotation. I strongly affirm that it has never been submitted in any university or any other institution of higher learning for the academic award.

Signature.....

Date.....

APPROVAL

This is to satisfy that this project by **YEKO ERIC** with the title **DESIGNING A FORWARD AND REVERSE BIASED DIODE** has been carried out under my supervision and in this day ready for submission to the Department of Physics at Busitema University for further examination.

Supervisor:

Name

Signature

Date

DEDICATION

I would like to dedicate this research project to my beloved parents **Mr. SOKUTON GEORGE** & **CATHERINE CHEROTICH** who have worked so hard end to end towards my education, may the almighty God reward them with better health forever.

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